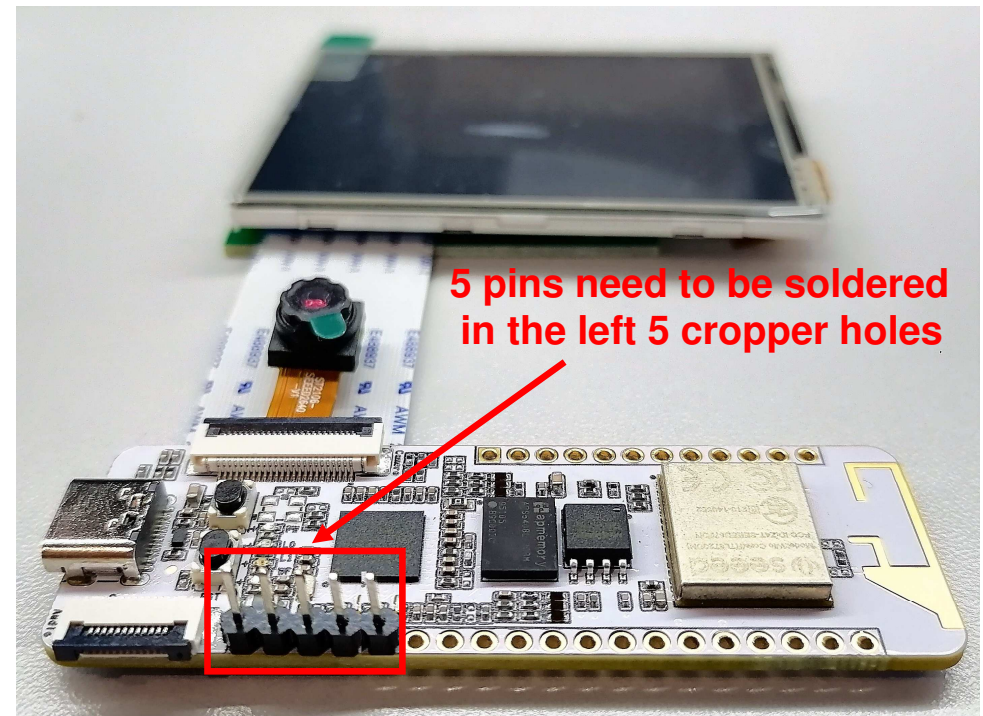


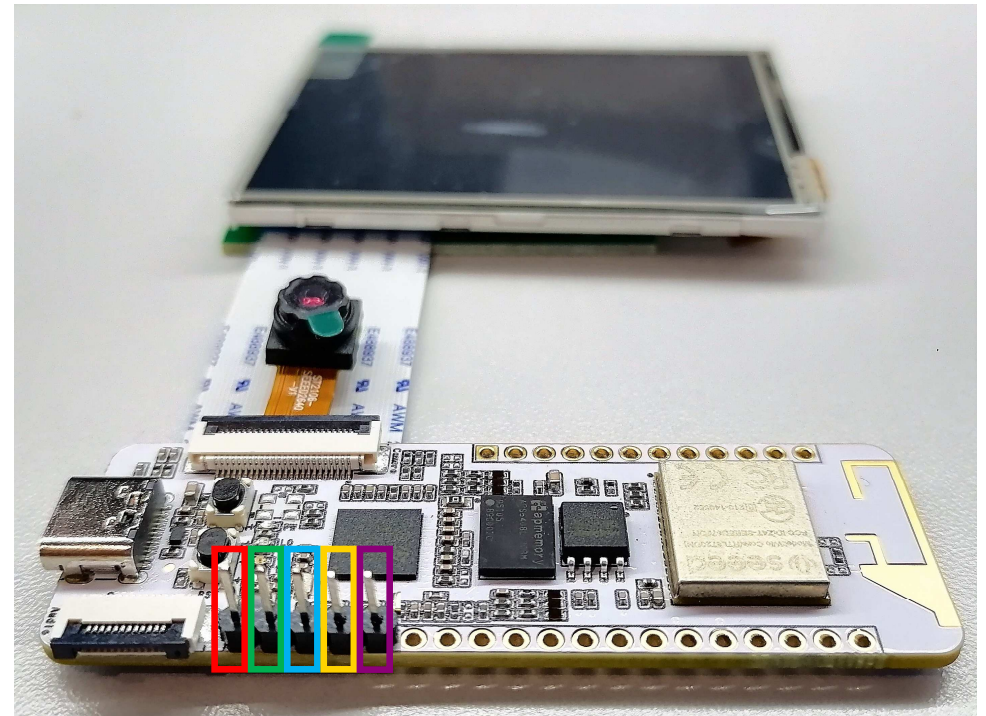
# Board Overview

- No pin soldered on Seeed board by default.
- Users need to solder 5 pins for SWD debug interface.
  - SWCLK
  - SWDIO
  - GND
  - RST
  - VDD (3.3V)



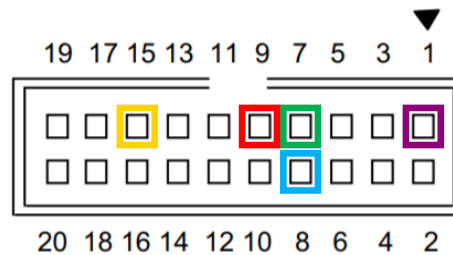
# Board Overview

- 5 pins correspond to signals with the same color:
  - SWCLK
  - SWDIO
  - GND
  - RST
  - VDD (3.3V)



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- SWCLK
- SWDIO
- GND
- RST
- VDD (3.3V)



# Connection

Use dupont wire female-to-female to connect the board with STLINK

