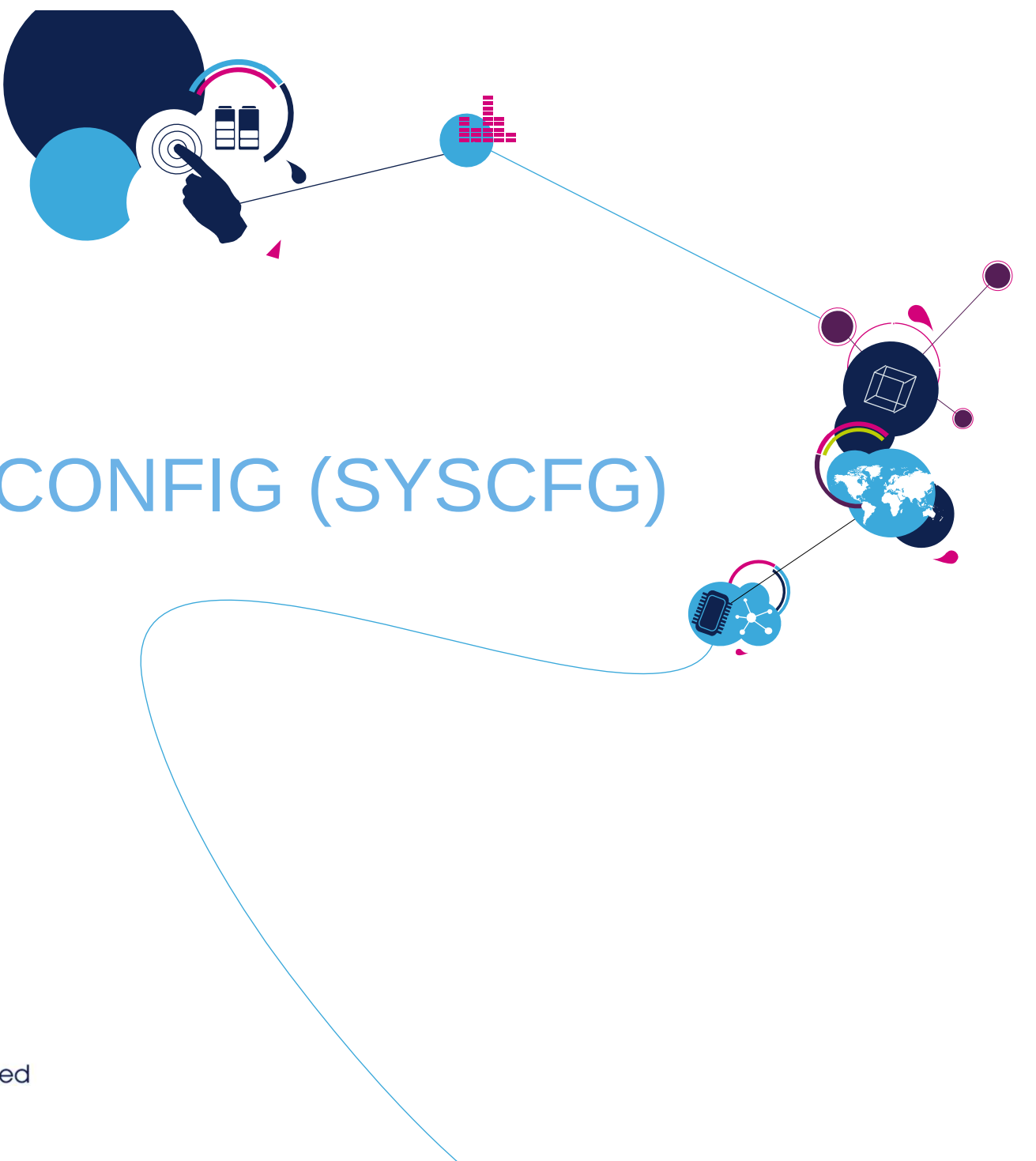




SYSTEM CONFIG (SYSCFG)

Speaker Name or Subtitle

- Memory area remap in SYSCFG_MEMRMP
 - Selection on the memory mapping at address 0x0000 0000 with MEM_MODE
 - Main Flash
 - System Flash
 - FMC bank 1 (NOR/PSRAM)
 - SRAM1
 - QUADSPI
 - Boosts up performance thanks to Icode/Dcode access instead of System Bus
 - FB_MODE
 - Used to swap flash bank 1 & 2
- Configuration register 1
 - FPU interrupts enable
 - I2C I/Os Fast-mode Plus 20 mA drive enable
 - PB6, PB7, PB8, PB8 high drive can be enabled even when not used for I2C
 - I/O analog switches voltage booster

- Manage external interrupt (EXTI) connection to GPIOx (x=A...H)
 - In SYSCFG_EXTICR1/2/3/4
- SRAM2 write protection and software erase
 - Erase control and busy flag In SYSCFG_SCSR
 - Erase control bit write access protected with a key written in SYSCFG_SKR SRAM2
 - 1KB granularity write protection in SYSCFG_SWPR
- Robustness features in SYSCFG_CFGR2
 - SRAM2 Parity error flag
 - ECC lock to connect Flash ECC error connection to TIM1/8/15/16/17 Break input
 - PVD lock to connect PVD interrupt to TIM1/8/15/16/17 Break input and to lock PVD enable and threshold
 - SRAM2 parity lock to connect SRAM2 parity error to TIM1/8/15/16/17 Break input
 - CLL lock to connect Cortex M4 LOCKUP (hardault) output to TIM1/8/15/16/17 Break input

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